

Form PTO-1449 (modified)

Atty. Docket No.
2000.051500.TT4092Serial No.
10/033,142

O I P
List of Patents and Publications for Applicant's
INFORMATION DISCLOSURE STATEMENT
OCT 07 2002
(Use several sheets if necessary)

Applicant
DALE E. GULICKFiling Date:
November 1, 2001Group:
2183

U.S. Patent Documents

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Foreign Patent Documents

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Other Art

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U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1						
	A2						
	A3						
	A4						
	A5						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						
	B2						
	B3						OCT 09 2002
	B4						Technology Center 2100
	B5						

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Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
SL	C1	Intel, "Low Pin Count (LPC) Interface Specification Revision 1.0," pp. 1-31 (09/29/97)
SL	C2	Standard Microsystems Corporation, "100 Pin Enhanced Super I/O for LPC Bus with SMBus Controller for Commercial Application," Part No. LPC47B37x, pp. 1-254 (06/17/99)
SL	C3	Intel, "Communication and Networking Riser Specification," Revision 1.0 (02/07/2000)
SL	C4	FIPS Pub 140-1 Federal Information Processing Standards Publication, "Security Requirements for Cryptographic Modules" (01/11/1994)
SL	C5	"Handbook of Applied Cryptography" CRC Press 1997 pp. 154 – 157, 160 – 161, 191 – 198, 203 - 212

EXAMINER: *Sumets Lefkowitz* DATE CONSIDERED: *6/7/04*

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SL	C6	DMTF, "Alert Standard Format (ASF) Specification" DSP0114, Version 1.03, 06/20/2001
SL	C7	Intel, "Advanced Configuration and Power Interface Specification," Revision 1.0b, 02/02/1999
SL	C8	Intel, "Advanced Configuration and Power Interface Specification," Revision 2.0, 07/27/2000
SL	C9	Intel, "Advanced Configuration and Power Interface Specification," Revision 2.0 Errata, Errata Document Rev. 1.5, 04/13/2001
SL	C10	Case, Fedor, Schoffstall, Davin, "A Simple Network Management Protocol (SNMP)" May, 1990
SL	C11	"Network Device Class Power Management Reference Specification," Version 210, 10/12/2000 pp. 1-9
SL	C12	Intel, "IPMI Intelligent Platform Management Interface Specification v 1.0," Document Revision 1.0, 09/16/1998
SL	C13	Intel, "IPMI Intelligent Platform Management Interface Specification v 1.5," Document Revision 1.0, 02/21/2001
SL	C14	Intel, "IPMI v 1.5 Addenda, Errata, and Clarifications – Intelligent Platform Management Interface Specification v1.5, revision 1.0," Addendum Document Revision 3, 05/16/2001
SL	C15	Intel, "IPMI Platform Management FRU Information Storage Definition v1.0," Document Revision 1.0, 09/16/1998
SL	C16	Socolofsky et al., "A TCP/IP Tutorial," January 1991
SL	C17	Intel, "IMPI Platform Management FRU Information Storage Definition v1.0, Document Revision 1.1, 09/27/1999
SL	C18	Intel, "Metolious ACPI/Manageability," Specification v1.0, 04/30/1999
SL	C19	Intel, "IPMI Intelligent Platform Event Trap Format Specification v 1.0," Document Revision 1.0, 12/07/1998
SL	C20	DMTF, "Common Information Model (CIM) Specification," DSP0004, Version 2.2, 06/14/1999
SL	C21	Intel, "SMBus Control Method Interface Specification," Version 1.0, 12/10/1999

EXAMINER: *Sumati Lefebvre*DATE CONSIDERED: *6/7/04*

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SL	C22	Intel, "System Management BIOS Reference Specification," Version 2.3.1, March 1999
SL	C23	Intel, "System Management Bus Specification," Revision 1.0, 02/15/1995
SL	C24	Intel, "System Management Bus (SMBus) Specification," SBS Implementers Forum, Revision 2.0, 08/03/2000
SL	C25	J. Postel, "User Datagram Protocol," ISI 08/28/1980
SL	C26	"About the 8051 Microcontroller," pp. 1-16
SL	C27	Intel, "8259A Programmable Interrupt Controller (8259A/8259A-2)," December, 1988
SL	C28	Intel, "Intel® 840 Chipset Platform Design Guide," October, 1999
SL	C29	AMD, "AMD's AlterIT™ Technology for Advanced Systems Management," Publication No. 22297 Rev. A, December, 1998
SL	C30	AMD, "AMD-766™ Peripheral Bus Controller Data Sheet," 23167B March, 2001

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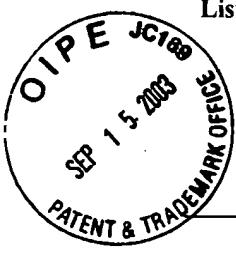
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EXAMINER: *Sumita Lefkowitz*

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6/7/04

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U.S. Patent Documents

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Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
SL	B1	WO 02086678A	10/31/2002				Yes RECEIVED
	B2						SEP 16 2003
	B3						
	B4						Technology Center 2100
	B5						

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
SL	C31	International Search Report PCT/US02/02980 dated 08/19/2003
	C32	
	C33	
	C34	
	C35	
	C36	

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